

fig. 1

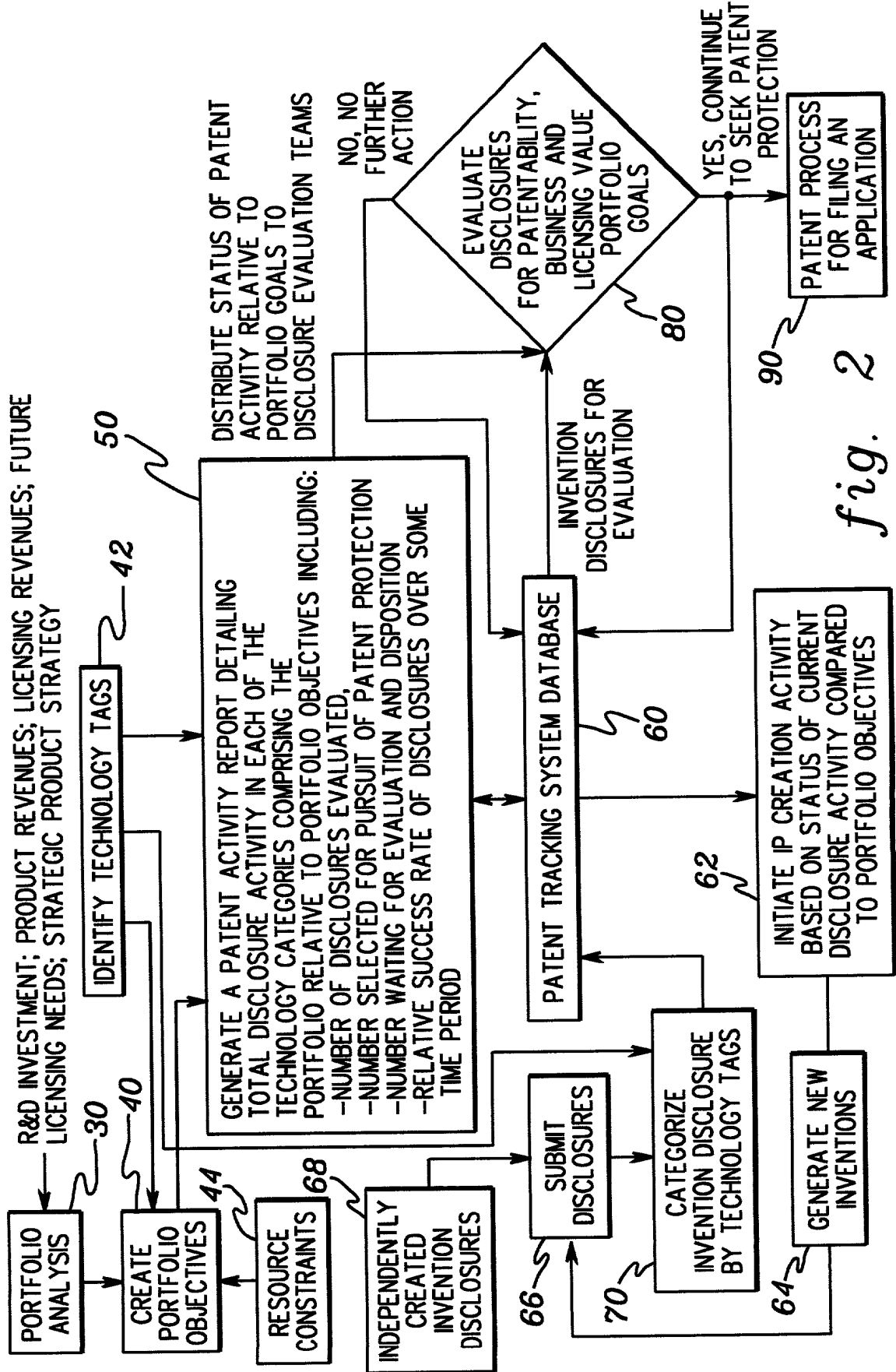


fig. 2

DISCLOSURE STATUS REPORT

MONTH, YEAR

TECHNOLOGY MAJOR CATEGORY	LOCATION	% RATED SEARCH LAST 12 MONTHS	% RATED SEARCH LAST 3 MONTHS	NUMBER CURRENTLY WAITING FOR EVALUATION	NUMBER EVALUATED YEAR TO DATE	NUMBER RATED SEARCH YEAR TO DATE	YEAR TO DATE/TOTAL YEAR GUIDELINE
SEMICONDUCTOR PROCESSING	A	50%	50%	1	2	1	
	B	56%	58%	40	170	92	
	C	77%	100%	1	10	7	
	D	70%	71%	95	251	179	
	TOTAL	64%	68%	137	433	279	226/271
SEMICONDUCTOR MANUFACTURING	A			1	0	0	
	B	27%	14%	22	40	10	
	C	67%	100%	0	3	2	
	D	60%	42%	9	42	25	
	TOTAL	45%	26%	32	85	37	24/29
SEMICONDUCTOR TESTING	A	50%		0	1	1	
	B	56%	73%	21	61	32	
	C	0%		0	0	0	
	D	52%	50%	3	28	15	
	TOTAL	54%	69%	24	90	48	47/57
CIRCUITS	A	97%	86%	2	29	28	
	B	73%	77%	9	79	59	
	C	75%		1	6	4	
	D	80%	44%	10	37	31	
	TOTAL	79%	71%	22	151	122	129/155
MEMORIES	A	100%		0	2	2	
	B	90%	100%	2	5	4	
	C	0		1	1	0	
	D	79%	85%	17	48	41	
	TOTAL	80%	86%	20	56	47	31/37

fig. 3A

DISCLOSURE STATUS REPORT

(CONTINUED)

MONTH, YEAR

TECHNOLOGY MAJOR CATEGORY	LOCATION	% RATED SEARCH LAST 12 MONTHS	% RATED SEARCH LAST 3 MONTHS	NUMBER CURRENTLY WAITING FOR EVALUATION	NUMBER EVALUATED YEAR TO DATE	NUMBER RATED SEARCH YEAR TO DATE	YEAR TO DATE/TOTAL YEAR GUIDELINE
INTERCONNECT TECHNOLOGY	A	100%		2	5	5	
	B	71%	100%	1	4	3	
	C	61%	57%	16	125	74	
	D	47%	52%	22	97	46	
	TOTAL	56%	55%	41	231	128	106/128
COMPUTER ARCHITECTURE	A	84%	67%	3	23	19	
	B	44%	50%	2	9	4	
	C	50%		0	2	1	
	D	100%		0	2	2	
	TOTAL	75%	60%	5	36	26	33/40
APPLICATIONS	A	79%	50%	2	24	19	
	B	44%	47%	14	65	28	
	C	94%	100%	1	12	11	
	D	56%	56%	16	28	16	
	TOTAL	58%	52%	33	129	74	72/87
NETWORKING	A	93%	93%	12	76	70	
	B	33%	43%	4	12	4	
	C	100%	100%	1	2	2	
	D	17%		5	11	1	
	TOTAL	79%	84%	22	101	77	74/88
ALL TECHNOLOGY TAGS	A	90%	86%	23	162	145	
	B	55%	55%	115	445	236	
	C	65%	64%	21	161	101	
	D	64%	66%	177	544	356	
	TOTAL	64%	65%	336	1312	838	743/891

fig. 3B

100

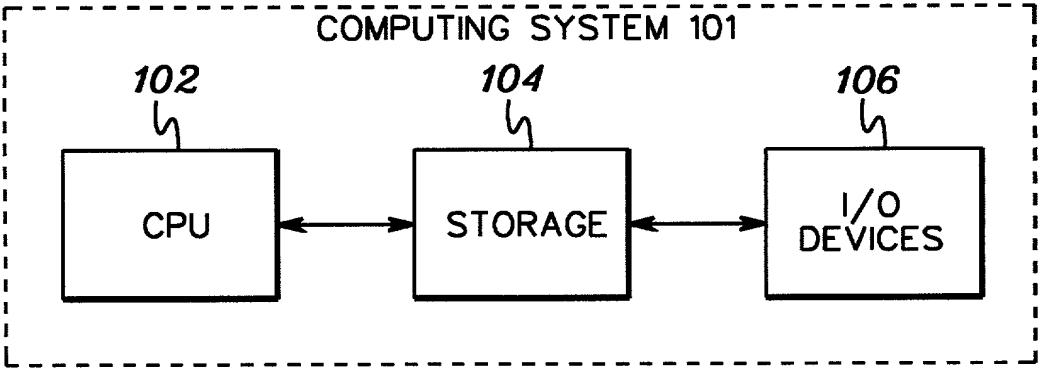


fig. 4